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Application Number:	10603251				
International Application Number:					
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Title of Invention:	Multiprocessor system with dynamic cache coherency regions				
First Named Inventor/Applicant Name:	Thomas J. Heller				
Correspondence Address:	Lynn L. Augspurger IBM Corporation 2455 South Road, P386 - Poughkeepsie NY 12601 US 8454331174 -				
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)				
1	Amendment - After Non-Final	POU920030123US1_Amend .pdf	373738	no	9				
'	Rejection		ec3b7cb6440ddfe27d718a36cd35d48d fa7a697d						
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National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.